

IN THE CLAIMS:

Claims 1-31. (Cancelled).

32. (New) An image display apparatus comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings; and
a driving circuit for applying a modulated signal having a pulsewidth corresponding to an image signal to each of said plurality of modulated signal wirings,
wherein said driving circuit causes the modulated signal to fall in discrete decrements to a non-display state from a display state.
33. (New) An image display apparatus comprising:
a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings;
a driving circuit for applying a modulated signal having a pulsewidth corresponding to an image signal to each of said plurality of modulated signal wirings, and
a switching circuit provided to each of the modulated signal wirings, adapted to change a signal level of the modulated signal in discrete decrements from a predetermined level of a display state to a predetermined level of a non-display state by way of an intermediate level having a predetermined time period between the predetermined level having the predetermined time period of the display state and the predetermined state of the non-display state.
34. (New) The apparatus according to claim 33, wherein said driving circuit has a plurality of charge paths for changing a signal level of the modulated signal and at least one of the plurality of charges paths is connected to a predetermined potential.

35. (New) The apparatus according to claim 34, wherein said driving circuit has a plurality of charge paths for changing a signal level of the modulated signal, and at least one of the plurality of charge paths is connected to a predetermined potential.

36. (New) The apparatus according to claim 34, wherein the plurality of charge paths have different change amounts per unit time of the signal level when the signal level is to fall.

37. (New) The apparatus according to claim 36, wherein the operation states of the plurality of charge paths are changed by exclusively operating charge paths having different change amounts per unit time of the signal level when the signal level is to fall.

38. (New) The apparatus according to claim 34, wherein the plurality of charge paths are arranged to operate in parallel, and the operation states of the plurality of charge paths are changed by changing the number of parallel-operating charge paths.

39. (New) The apparatus according to claim 34, further comprising a circuit for determining the operation states of the plurality of charge paths.

40. (New) The apparatus according to claim 32, wherein said driving circuit comprises a rise circuit for raising a signal level and a separate fall circuit for causing the signal level to fall.

41. (New) The apparatus according to claim 32, wherein each said display device comprises an electron-emitting device.

42. (New) An image display method of driving a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings, comprising the steps of:

applying a modulated signal having a pulsewidth corresponding to an image signal to each of the plurality of modulated signal wirings; and

causing the modulated signal to fall in discrete decrements to a non-display state from a display state.

43. (New) An image display method of driving a plurality of display devices wired in a matrix through a plurality of scanning signal wirings and a plurality of modulated signal wirings, comprising the steps of:

applying a modulated signal having a pulsewidth corresponding to an image signal to each of the plurality of modulated signal wirings; and

changing a signal level of the modulated signal in discrete decrements from a predetermined level of a display state to a predetermined level of a non-display state by way of an intermediate level having a predetermined time period between the predetermined level of the display state and the predetermined state of the non-display state.